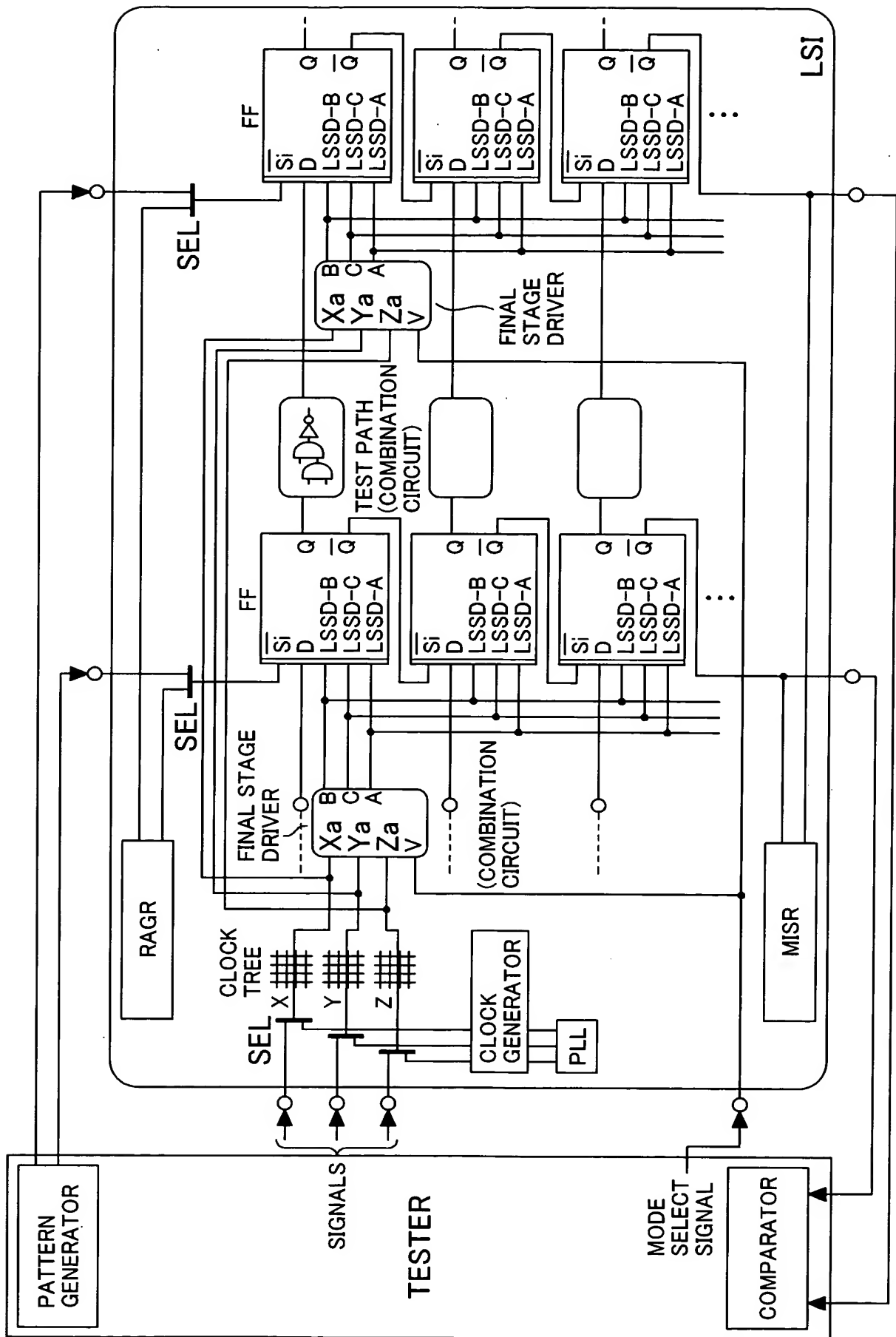
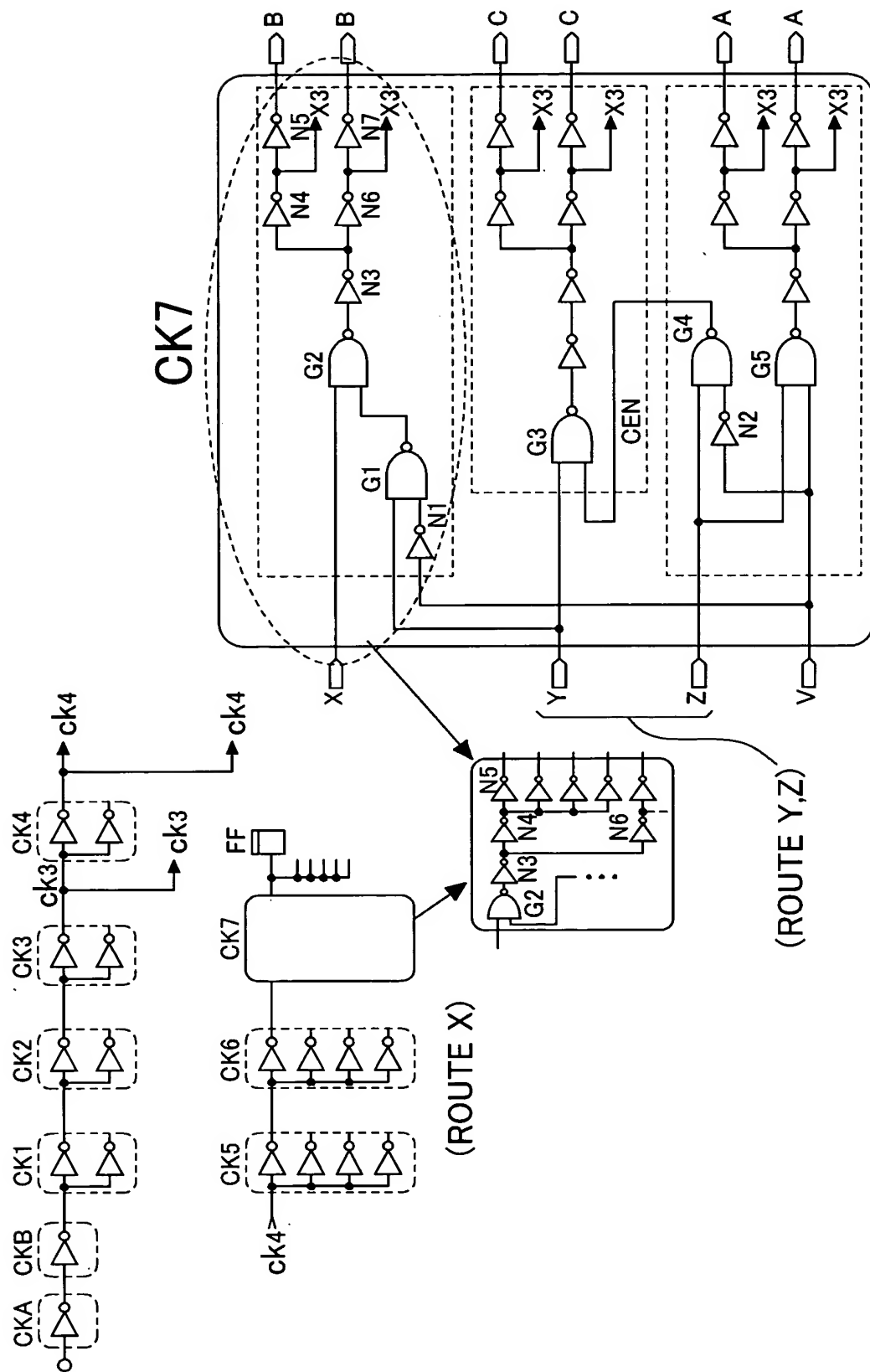


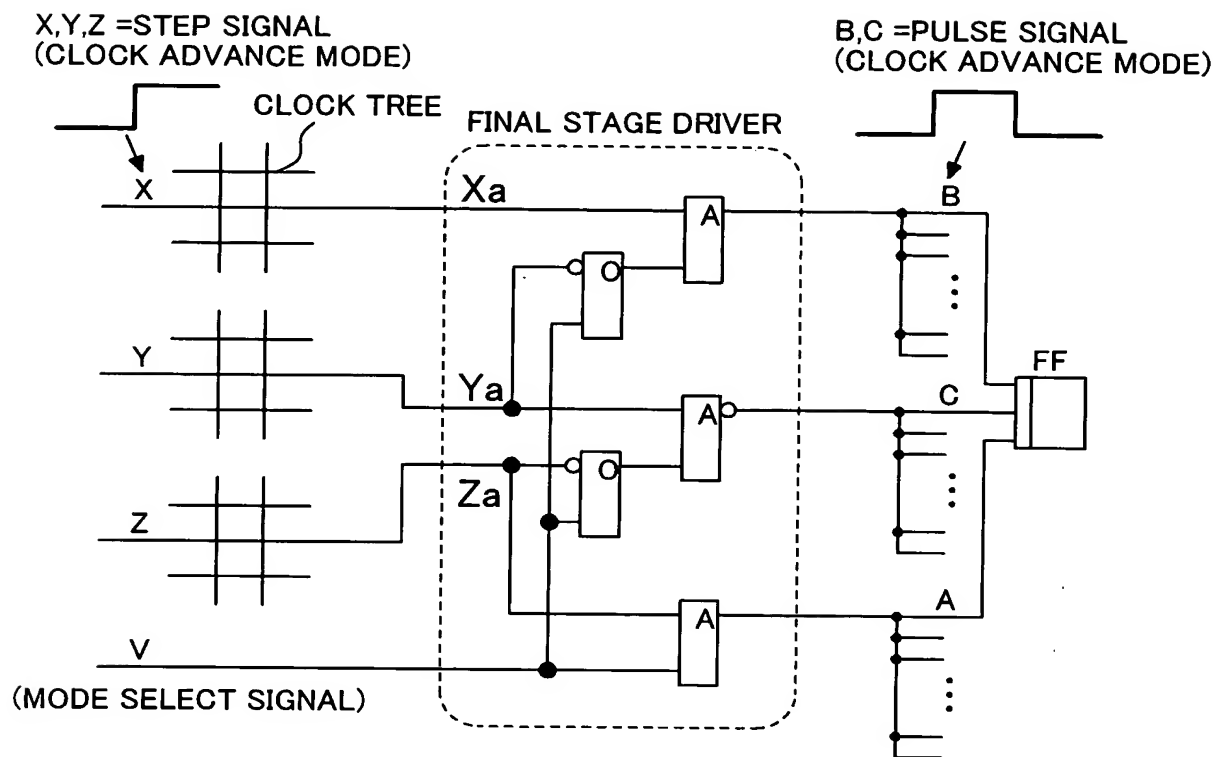
FIG.1



**FIG. 2**

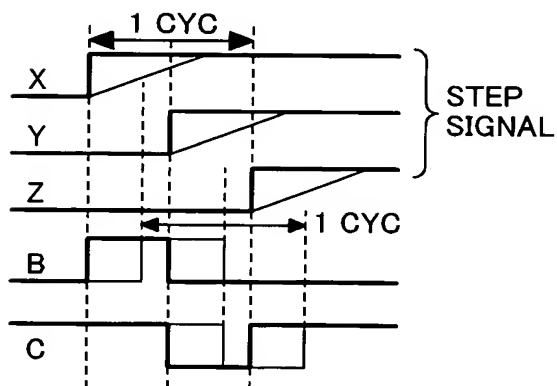


# FIG.3



# FIG.4

CLOCK ADVANCE MODE (V='L')



THROUGH MODE (V='H')

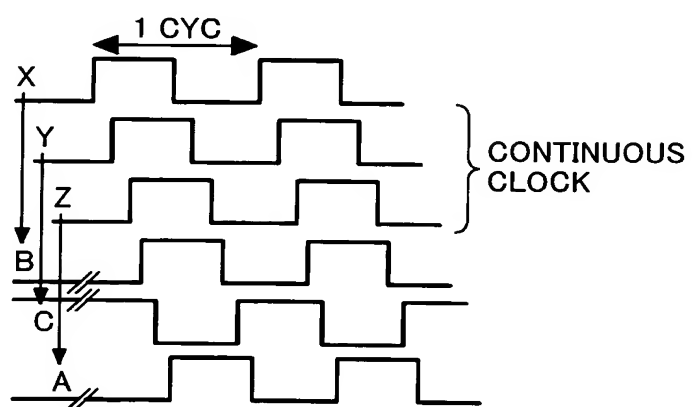


FIG.5

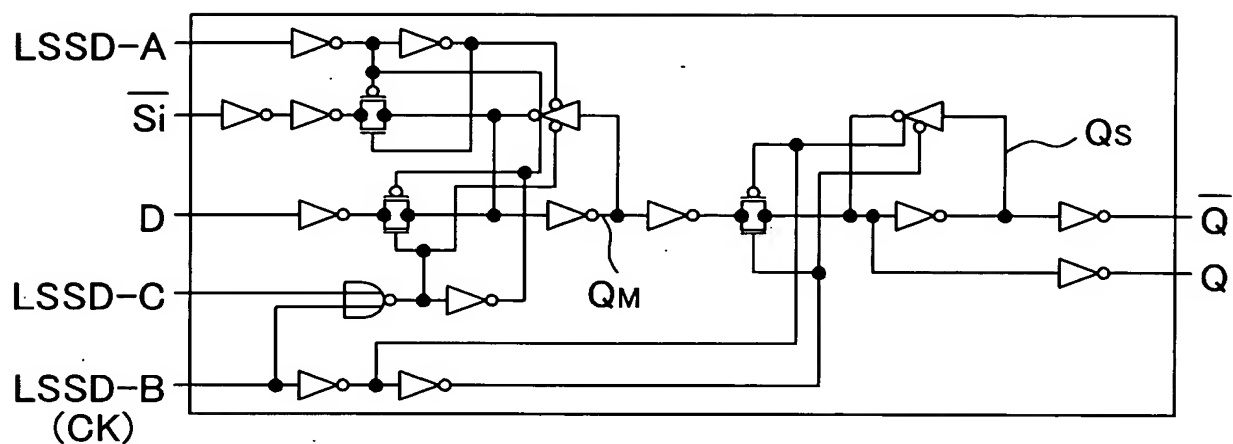


FIG.6

INPUT					INTERMEDIATE NODE		OUTPUT		NOTE
CK	D	LSSD-A	LSSD-C	$\overline{Si}$	QM	Qs	Q	$\overline{Q}$	
L	*	L	L	*	D	Qs-1	Qs	$\overline{Qs}$	NORMAL
H	*	L	L	*	QM-1	QM	Qs	$\overline{Qs}$	NORMAL
	L	L	L	*	L	QM	L	H	NORMAL
	H	L	L	*	H	QM	H	L	NORMAL
L	*	L	H	*	QM-1	Qs-1	Qs	$\overline{Qs}$	DIAGNOSTIC
H	*	L	H	*	QM-1	QM	Qs	$\overline{Qs}$	DIAGNOSTIC
L	*	H	H	L	H	Qs-1	Qs	$\overline{Qs}$	DIAGNOSTIC
L	*	H	H	H	L	Qs-1	Qs	$\overline{Qs}$	DIAGNOSTIC
*	*	H	L	*					PROHIBITED
H	*	H	H	*					PROHIBITED

# FIG.7

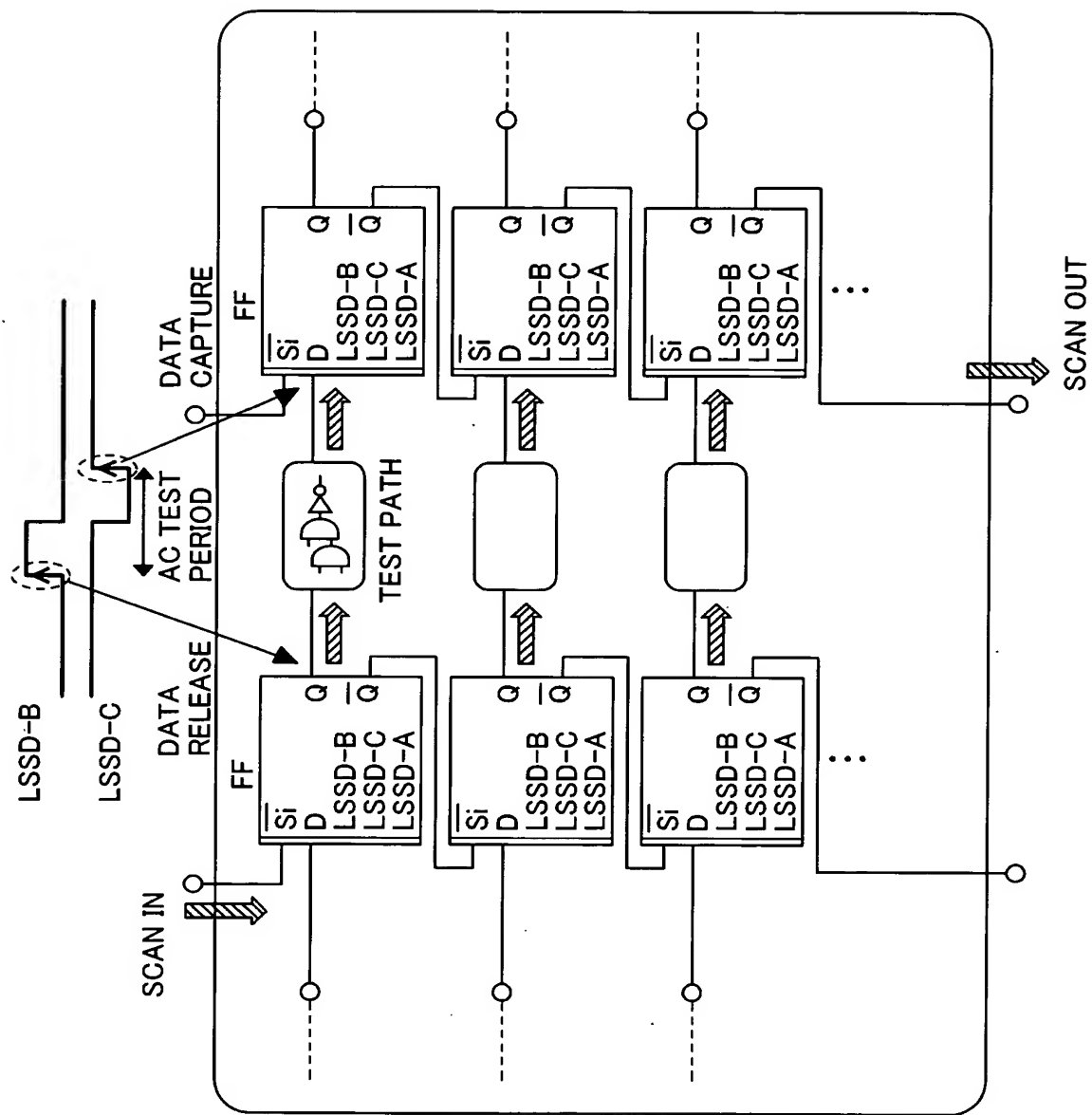
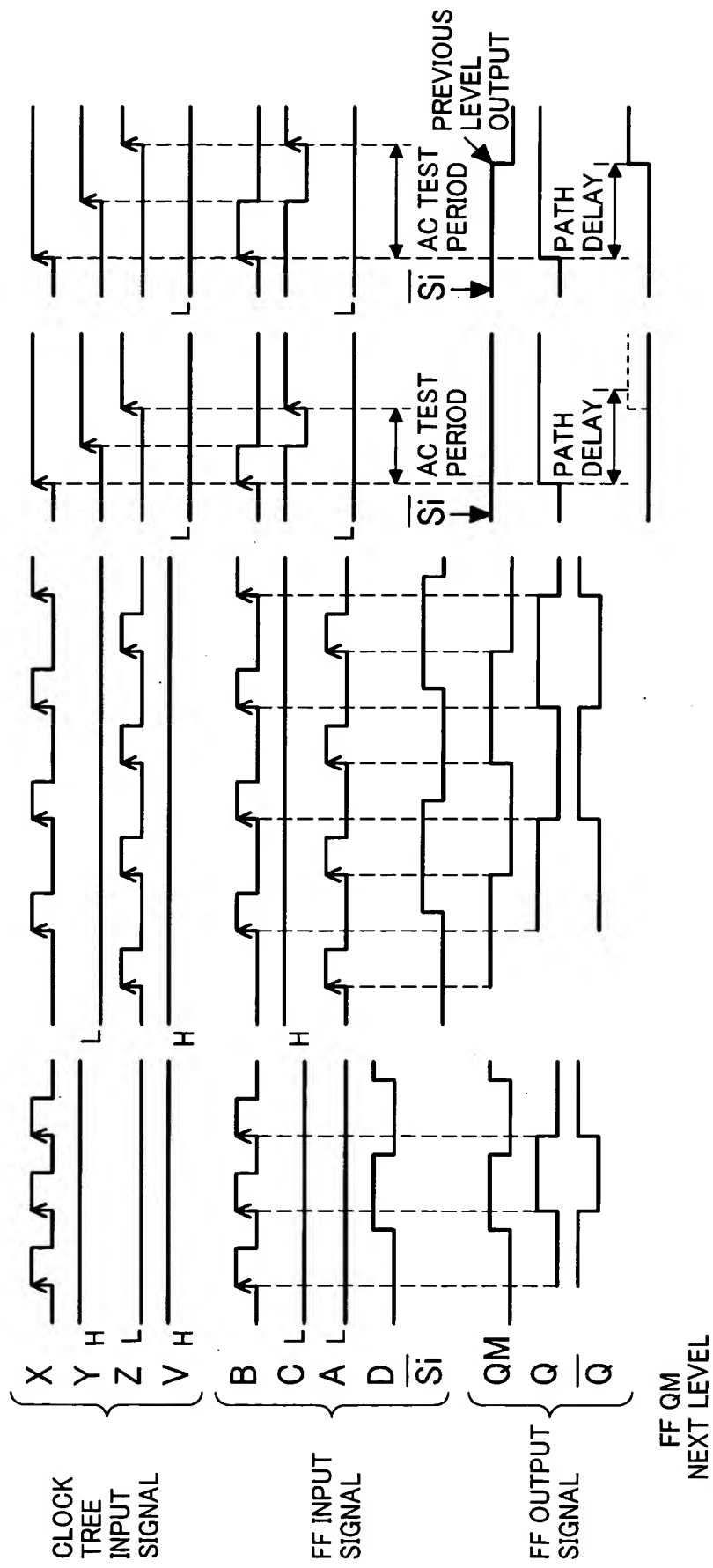


FIG.8A

FIG.8B

FIG.8C

FIG.8D



# FIG.9

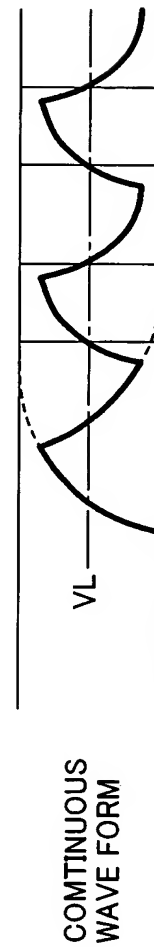
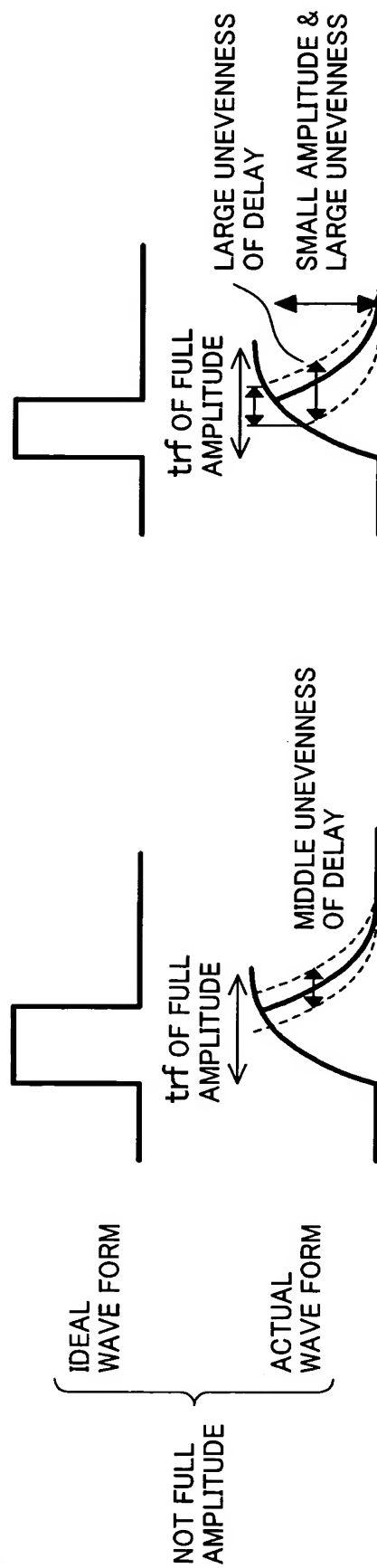
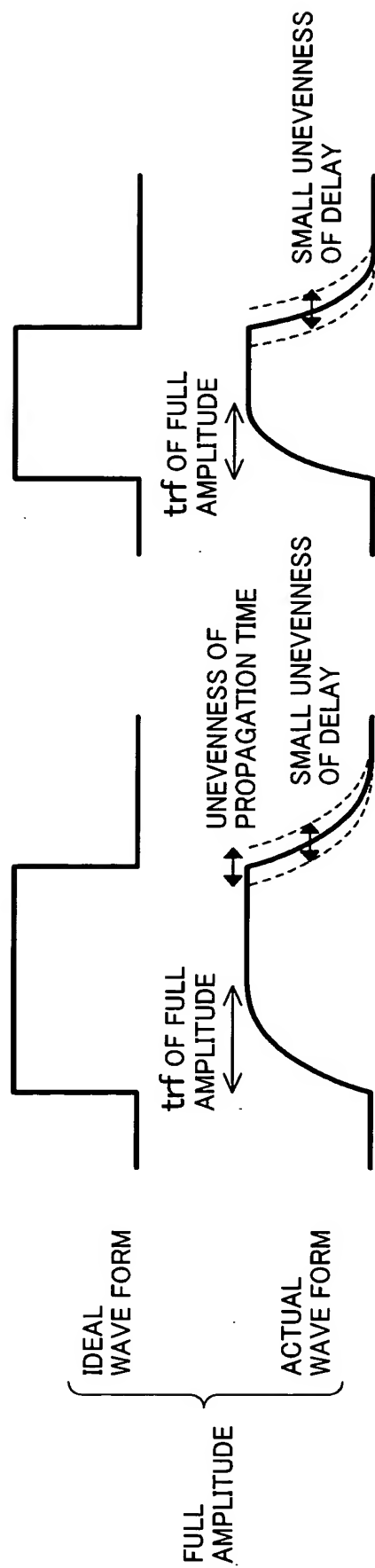


FIG.10

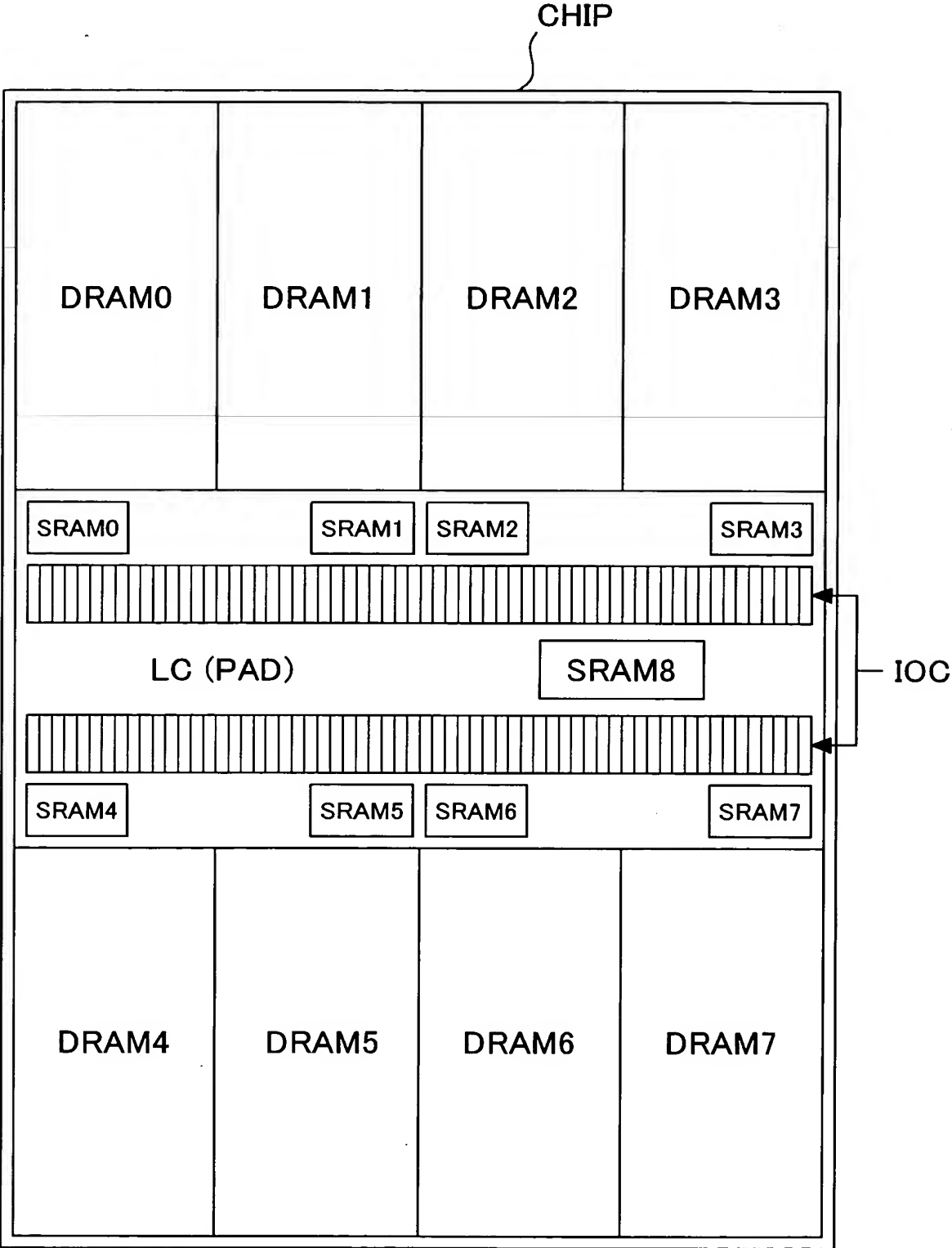




FIG.11

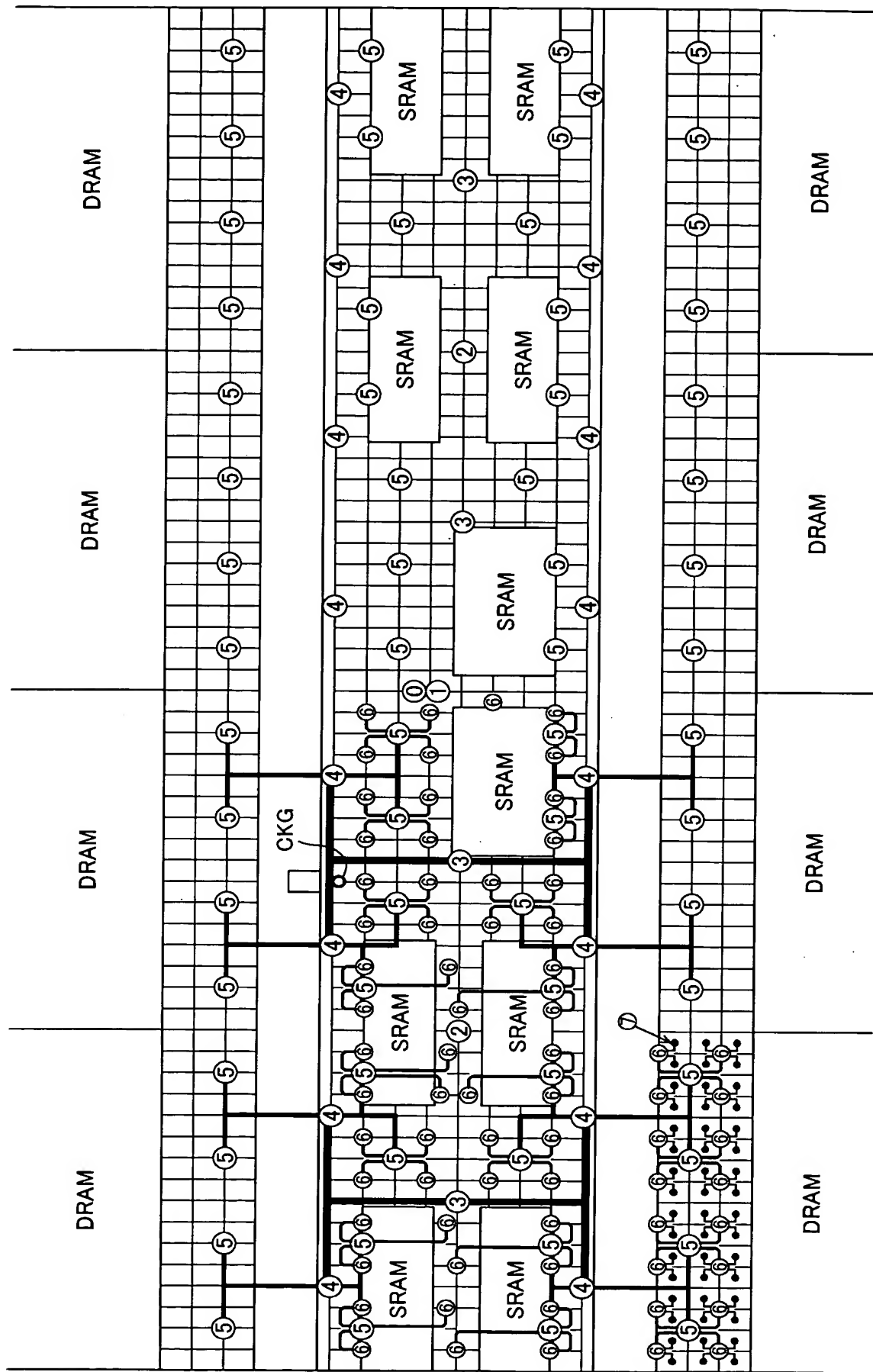


FIG.12

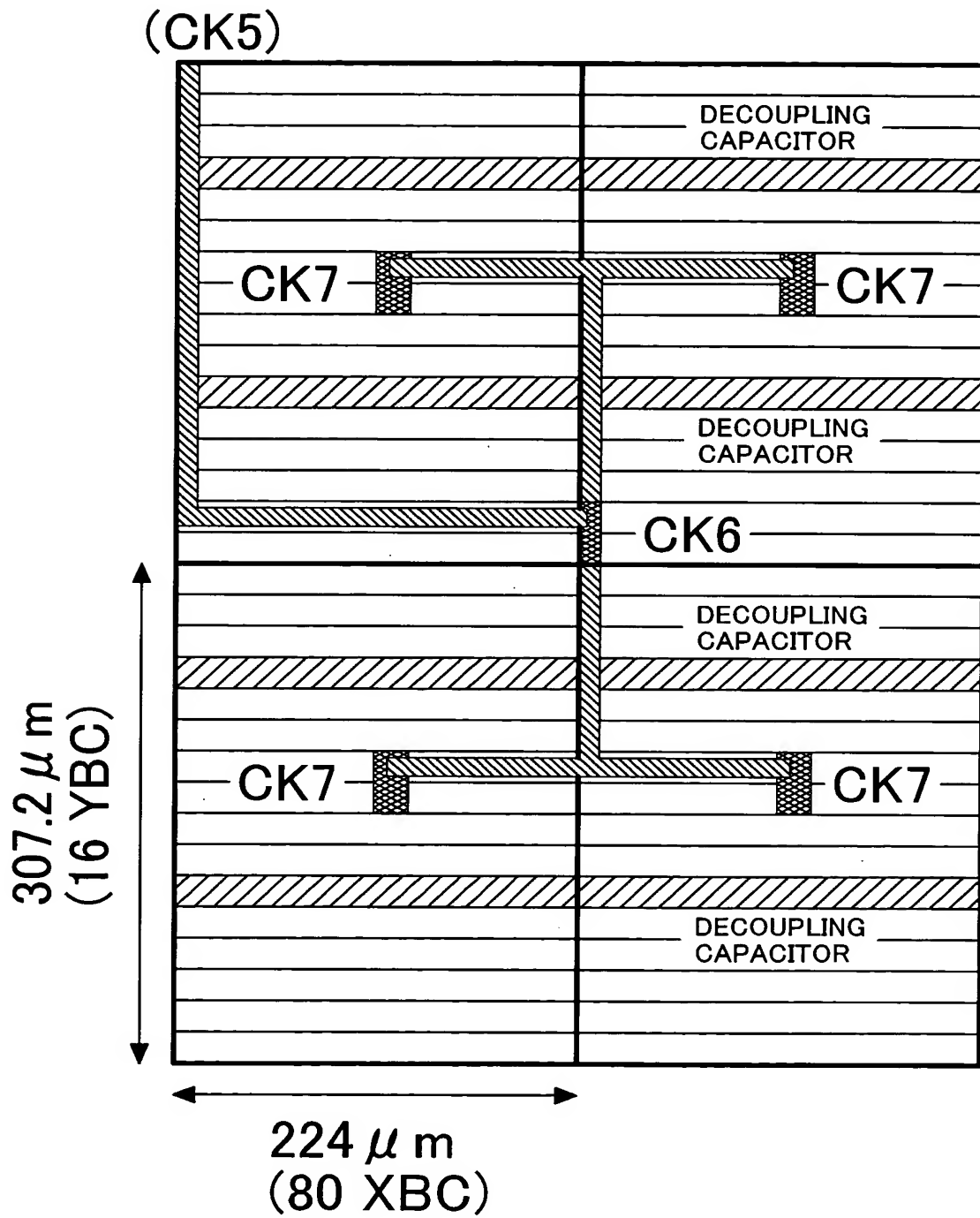


FIG.13

